EC Department,L.D.College of Engg.,Ahmedabad

Report on Industrial Visit to e-Infochips,Ahmedabad
(18-02-2020,29-2-2020)

An industrial was arranged to eInfochips,Aryan Complex,Near Shilaj Railway crossing, Ahmedabad.The visit was as per the following schedule:

Sr.No.	Semester	Date	Faculty
1	BE-Sem-VI(EC)-Div A	18-2-2020	Prof.Arun Nandurbarkar, Prof.Sanjay Prajapati
2	BE-Sem-VI(EC)-Div B	29-2-2020	Prof.Panakaj Prajapati, Prof.Kirit Patel

eInfochips, an Arrow company, is a leading global provider of product engineering and semiconductor design services. With over 500+ products developed and 40M deployments in 140 countries. The company's service offerings include digital transformation and connected IoT solutions across various cloud platforms, including AWS and Azure.

Its an ASIC design service company, outsourcing from specification to a working silicon. With over 200+ silicon tape-outs, we have the expertise to deal with increasing complexity of Digital and Mixed/Signal SoC's design and fuctional verification.

Mr. Chintan Panchal and Mr. Snehal Patel, who both are LDCE Alumini and are right now serving einfochip gave an overview of the ASIC industry and eInfochips. They explained both front end designing and back end designing for VLSI chips and how in industry this designing takes place. Also they showed how their company expertise in software, system, silicon and mechanical fields. The whole concept of how silicon chips are made from sand was made cleared through an intel 22nm chip's video. They also showed how along with technical skills, one company requires some moral values also and have shown their core values. Clintele was shown which included companies like TOSHIBA, Teradyne, Faraday, Texas, Qualcomm, Honeywell, Google, etc. There was an informative question and answer session.

This continued with the second session, which was conducted by Mrs. Charu Patel , who is an alumini of Charusat University and is right now serving einfochip and is an expert IN DFT. She explained how on the back end side, testing and verification is done in industry. Also the difference between testing and verification was explained . Also how the errors are detected and their solution was done was shown with an example which cleared the concept. This session ended with an

interactive question and answers session.

After this, project demonstration was done where projects on networking, automotive, smartphones, multimedia, aerospace, servers, sensors, automated test equipment, home automation, VR glasses, healthcare, etc.

The lunch for all the students were also arranged by the company and after the lunch one informative small question answer session for career, jobs, placements and company related was taken by Mr. Nilesh Ranpura.

Overall as students were guided for their future by experts and the knowledge of industrial work for VLSI domain was understood, and the main purpose of industrial visit was fulfilled.

The participant students and the faculties got good knowledge about the challenges in VLSI.

The industrial visit was coordinated by Prof.Arun Nandurbarkar.

The department is thankful to Prof. Mihir V. Shah,Head of the Department for encouraging to arrange this industrial visit. The department is also thankful to the Principal Dr. R.K.Gajjar for giving permission to organize the industrial visit.