Name of the event	**Hands on workshop on RISC-V based Education board "VSDSquadron"**
Duration	27th July 2023
Venue	1. Bhaskarcharya hall, EC Department, LDCE, Ahmedabad 2. Embedded systems laboratory, EC Department, LDCE, Ahmedabad
Organized by	EC Department , LDCE, Ahmedabad in coordination of e infochips
Name of coordinators	Dr. Pankaj P Prajapati
Name of expert	Shri Kunal Ghosh, (co-founder and Director of VLSI System Design (VSD) Corp. Pvt. Ltd) Shri Pranav Vyas(Associate director-e-Infochips) and Shri Sharat Kaul (Vice president, IESA) and their team
Organized by	Department of Electronics and Communication, L D College of Engineering
Objective of Programme	The motive of the workshop was to spread awareness among the students about RISC-V Processor.
Outcome of Programme with Report	The participants motivated and encouraged to learn about the futuristic area of RISC-V. Students learned about design to tapout and entire chip design flow.

A workshop on "RISC-V based Education board" was conducted on the 27th of July 2023 at Bhaskarcharya Hall, Department of Electronics and Communication of L D Engineering College in association with e-Infochips. About 75 students of the department and faculty members attended the workshop. The motive of the workshop was to provide the information about the research in the area of VLSI. Shri Pranav Vyas(Associate director-e-Infochips), Shri Kunal Ghosh (CEO,VSD) and Shri Sharat Kol(Vice president, IESA) explained about the research going on in the area of VLSI.

In the workshop, the experts from different organizations explain the RISC-V processors and the students did hands on practice also. Student learnt about the complete process starting from product specifications to chip design. The workshop was very useful to the students.

Feed back	The event was very effective for the students and they learnt a lot from this event.
Future action based on Programme	In future, this type of workshops in the area of VLSI and chip design will be arranged to share the knowledge and contribution of the students in the futuristic area of VLSI.
Number of Participant	About 75 Students

Photographs: